SE_E&TC May_2020 ELECTRONIC DEVICES AND CIRCUITS-I CBCGS-HSUBJECT-CODE-PCC-ETC-301 Marks: 50

1.	An intrinsic semiconductor at absolute zero temperature acts as (1M)			
	a)	Insulator		
	b)	Conductor		
	c)	Superconductor		
	d)	Rectifier		
2.	A trivalent atom used for doping in semiconductor is called (1M)			
	a)	Accepter		
	b)	Donor		
	c)	Catalyst		
	d)	Fermi level		
3.	Doping of trivalent atom leads to creation of extrinsic semiconductor.			
	a)	P type		
	b)	N type		
	c)	Junction		
	d)	Barrier potential	(1M)	
4.	A pure	, undoped semiconductor material is called as semicondu	uctor.	
	a)	Intrinsic		
	b)	Extrinsic		
	c)	Accepter		
	d)	Donor		
		(1M)		
5.	Drift current is due to			
	a)	Applied voltage		
	b)	Difference in carrier concentration		
	c)	Temperature		
	d)	Barrier potential	(1M)	
6.	A Gern	A Germanium diode contains volt as cut-in voltage. (1M)		
	a)	0.3		
	b)	0.7		
	c)	1		
	d)	1.3		
7.	The leakage current in a diode is due to			
	a)	minority carriers		
	b)	majority carriers		
	c)	junction capacitance		
	d)	doping		
8.		diode is operated in condition for voltage regulation	(1M)	
	a)	Forward biased		

	c) d)	Short circuited Unbiased condition	
9.	-	ing point in a working transistor amplifier should be at when used cut off region	as amplifier.
	b)	saturation region	
	c)	mid of load line	
	d)	anywhere	(2M)
10.	BJT is a	device.	(1M)
		Unipolar	
		Bipolar	
	•	Passive	
	d)	Active unipolar	
11.		se current in a transistor is very small and is of the order of	(1M)
	-	mA A	
	b)	uA	
	-	kA	
42	15.0 21	50 and ID is 10 v.A. then IC	(20.4)
12.	-	50 and IB is 10 uA, then IC = 2.5 mA	(2M)
	•	25 mA	
		10 mA	
	d)	1 mA	
13	In a fixed bias common emitter circuit RB= 250 KΩ, Vcc= 5, and VBE= 0.7 V then IB =		
10.		17.2 uA	
	•	28.8 uA	
	c)	38.8 uA	
	d)	48.8 uA	(2M)
14.	In a fixe	ed bias common emitter circuit RB= 100 K Ω , Vcc= 5, B(beta) = 200 and VI	BF= 0.7 V
		=	•
		8.6 mA	
	-	10.6 mA	
	c)		
	d)	14.6 mA	(2M)
15		model is most suitable model of transistor for its ac analysis.	(1M)
		Hybrid Pi	(=:-/
	-	T-model	
	c)	z- model	

b) Reverse biased

	d)	Y- model	
16.	Output	ac resistance of a bipolar transistor for given VA(Early voltage) = 35 V and	d collector
	current	: ICQ= 1 mA is	(2M)
	a)	35 k Ω	
	b)	40 k Ω	
	c)	45 k Ω	
	d)	50 k Ω	
17.	rπ =	for ICQ= 0.5 mA and B(beta)= 100 with VT= 26 mV.	(2M)
	a)	5.2 kΩ	
	b)	4.28 kΩ	
	c)	3.28 kΩ	
	d)	6.28 kΩ	
18.	Assume	e that ICQ = 0.5 mA and VT= 26 mV then gm=	(2M)
	a)	21.23 mA/V	
	b)	19.23 mA/V	
	c)	22.23 mA/V	
	d)	23.23 mA/V	
19.	rπ =	for ICQ= 0.5 mA and B(beta)= 350 with VT= 26 mV.	(2M)
	a)	18.2 kΩ	
	b)	12.4 kΩ	
	c)	14.4 kΩ	
	d)	16 kΩ	
20.	JFET is	a device.	(1M)
	a)	Unipolar	
	b)	Bipolar	
	c)	Passive	
	d)	Active bipolar	
21.	In P-ch	annel JFET, current carriers are	(1M)
	a)	Holes	
	b)	Electrons	
	c)	Positive ions	
	d)	Negative ions	
22.	The gate in a JFET controls (1M)		(1M)
	_	The width of the channel	,
	•	Output voltage	
		Input voltage	
		Recombination	
22	For a n-channel JFET, IDSS= 2 mA, Vp= -3.5 V and VGS= 0 V then ID= (2M)		
25.	a)	2 mA	(2141)
	-	3 mA	
	•	4mA	
	•	5 mA	
21	•		(114)
∠4.		transistor, Gate and Source are biased. Reverse	(1M)
	a) b)	Forward	
	· ·		
	c)	Independently	

	d)	Not	
25. Drain current in a JFET for a given value of $Vp=-3.5\ V$, IDSS = 5 mA and $VGS=V$			o/2 is
	a)	1.25 mA	
	-	2.25 mA	
	•	3.25 mA	
	d)	4.25 mA	(2M)
			(45.4)
26.		out impedance of an ideal FET is	(1M)
	•	Zero Ω	
	•	Infinite Ω	
		100 ΚΩ	
	d)	500 kΩ	
27	Lower:	and upper cut off frequencies of an amplifier is 0 Hz and 2 kHz therefore	handwidth of
_,.		plifier is	barrawraerr or
		2 kHz	
	-	1kHz	
	•	1.5 kHz	
	•	4 kHz	(2M)
28.	•	gain in an amplifier is given as	(1M)
	_	Vout/Vin	(=)
	-	Vin/Vout	
	-	lout/Vin	
	-	Vout/lin	
29.	9. Voltage gain of an amplifier is 150 and, input applied voltage is vi= 0.5 mV therefore output		efore output
	voltage	is	
	a)	75 mV	
	b)	85 mV	
	c)	95 mV	
	d)	100 mV	(2M)
20	14.		(4.0.4)
30.		n capacitors are while calculating mid band amplifier gain. Treated short	(1M)
	a) b)		
	-	Treated open	
		Replaced with resistors	
	u)	Replaced with current sources	
31.	For a D	arlington pair of transistors which relation is correct?	(1M)
		erall current gain, B1 and B2 are gain from individual amplifier)	, ,
		B = B1*B2	
	•	B= B1+B2	
	•	B= B1-B2	
	d)		
32.	 In a Darlington pair transistor, emitter of first transistor is connected to the of second 		
	transistor.		
	a١	Fmitter	

	b)	Base	
	c)	Collector	
	d)	Emitter and base	(1M)
22	A Darli	agton transistor has	(111)
33.		ngton transistor has	(1M)
		A very low input impedance	
	•	Three transistors	
	c)	A very high current gain	
	d)	One VBE drop	
34.	Let bet	a B1=250 and B2= 250, then overall current gain in a Darlington pair trans	sistor is
		62,500	
	b)		
	c)	500	
	d)	1	(2M)
35.		a B1=150 and B2= 150, then overall current gain in a Darlington pair trans	sistor is
	 a)	22500	
	b)	300	
	c)	1	
	d)	0	
	/		(2M)